Electronic Pater	t App	olication Fe	e Transm	ittal			
Application Number:	10	10715746					
Filing Date:	17	17-Nov-2003					
Title of Invention:	wi	Method and apparatus for providing an inter integrated circuit interface with an expanded address range and efficient priority-based data throughput					
First Named Inventor/Applicant Name:	TH	Thomas E. Baker					
Filer:	Th	Theodore Naccarella/Patricia Frisoli					
Attorney Docket Number:	Ba	Baker 1-11-13					
Filed as Large Entity							
Utility Filing Fees							
Description		Fee Code	Quantity	Amount	Sub-Total in USD(\$)		
Basic Filing:					1		
Pages:							
Claims:		7.2					
Independent claims in excess of 3		1201	5	200	1000		
Miscellaneous-Filing:							
Petition:							
Patent-Appeals-and-Interference:							
Post-Allowance-and-Post-Issuance:							
Extension-of-Time:							

Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
Extension - 2 months with \$0 paid	1252	1	450	450
Miscellaneous:				
	Tota	1450		